

Integrated Informatics Design Area, HIROSE Lab. (<http://ssc.eei.eng.osaka-u.ac.jp>)

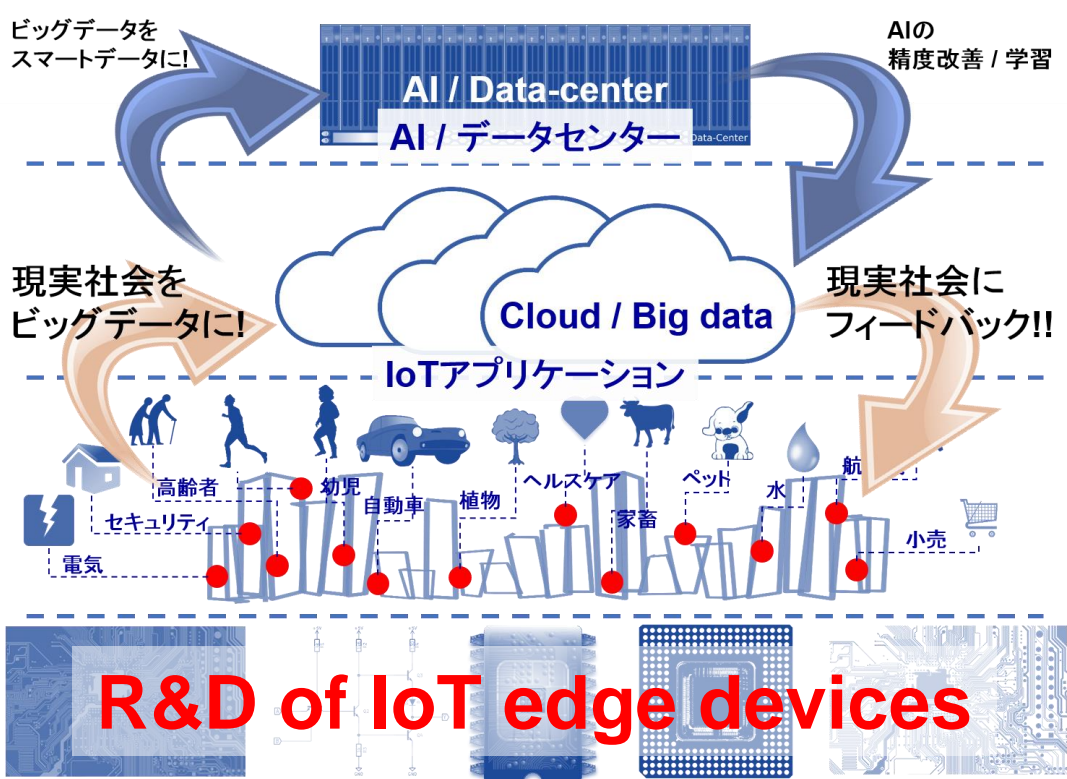
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To achieve high performance large scale integration (LSI) systems that will be fundamental key devices for next generation IoT/AI society, we develop an ultra-low power and low voltage LSI, energy harvesting systems, and the wide-gap semiconductor devices.

LSI systems operating with subtle environmental energy (Harvesters: small PV cell, TEG, etc.)

Power management LSIs

Development of Integrated functional systems



2mm

180nm CMOS Process

2mm

65nm CMOS Process

Development of Ultra-low Power nano-watt LSIs