Graduate school of Engineering, Division Electric, Electronics, and Infocommunications Engineering

Integrated Information Design Area, HIROSE Lab.

(http://ssc.eei.eng.osaka-u.ac.jp)

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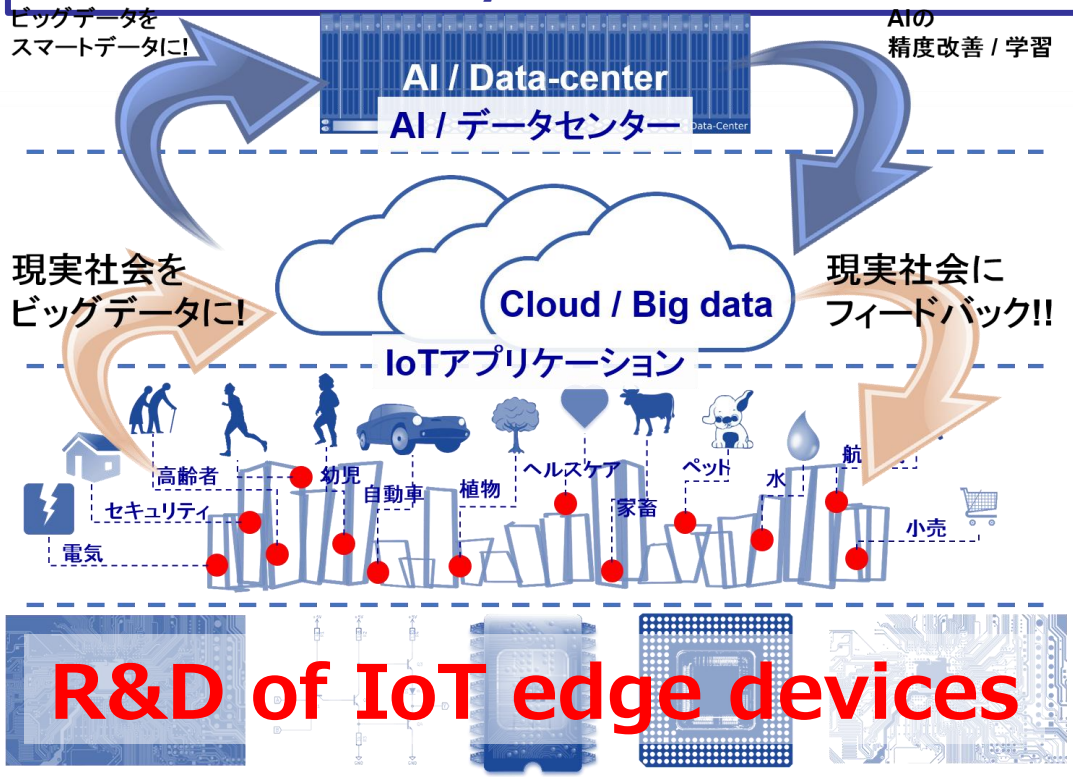
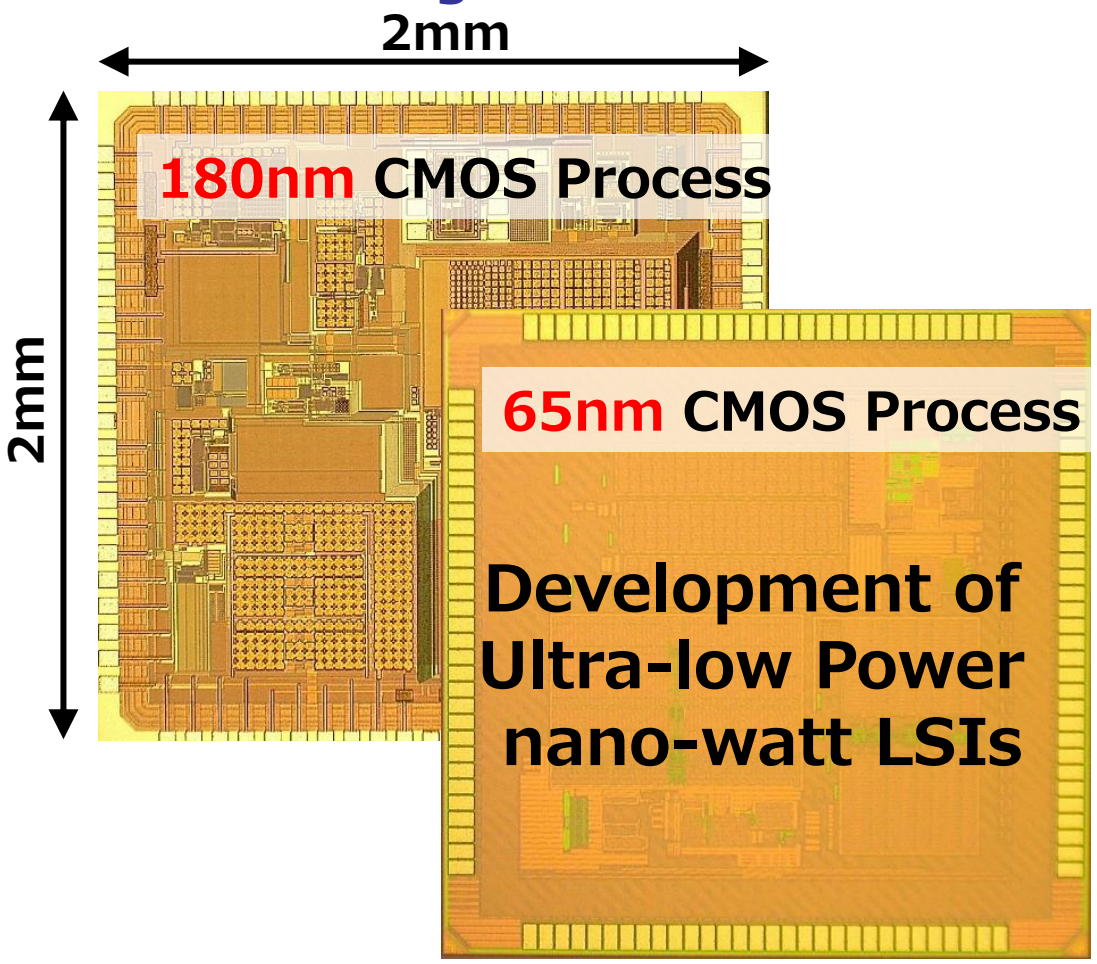
To achieve high performance LSI systems that will be fundamental key devices for next generation IoT/AI society, we develop an ultra-low voltage and ultra-low power LSI, energy harvesting systems, and biomedical sensing systems.

Development of Integrated functional systems



LSI systems operating with subtle environmental energy (Harvesters: small PV cell, TEG, etc.)

Power management LSIs